

L Number	Hits	Search Text	DB	Time stamp
21	186	@ad<=20000106 and 710/35.ccls.	USPAT; US-PGPUB	2002/12/12 15:29
24	12	@ad<=20000106 and 710/35.ccls. and (coprocessor co-processor)	USPAT; US-PGPUB	2002/12/12 15:20
22	274	@ad<=20000106 and 710/33.ccls.	USPAT; US-PGPUB	2002/12/12 15:29
23	292	@ad<=20000106 and 710/110.ccls.	USPAT; US-PGPUB	2002/12/12 15:28
28	146	@ad<=20000106 and 710/35.ccls. and burst	USPAT; US-PGPUB	2002/12/12 15:29
27	66	@ad<=20000106 and 710/33.ccls. and burst	USPAT; US-PGPUB	2002/12/12 16:01
26	69	@ad<=20000106 and 710/110.ccls. and burst	USPAT; US-PGPUB	2002/12/12 15:29
29	30	@ad<=20000106 and burst with ((master and slave) coprocessor co-processor) and (increment with address) and (register with control) and ((mode burst) with bit)	USPAT; US-PGPUB	2002/12/12 16:03
	169	712/34.ccls.	USPAT; US-PGPUB	2002/08/13 15:12
	275	712/32.ccls.	USPAT; US-PGPUB	2002/08/14 08:18
	147	345/503.ccls.	USPAT; US-PGPUB	2002/08/12 15:07
	3757	(co\$1processor (dual adj1 processor)) and address and data and (burst single)	USPAT; US-PGPUB	2002/08/12 16:31
	1574	(co\$1processor (dual adj1 processor)) and address and data and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/12 16:33
	1245	(co\$1processor (dual adj1 processor)) and address and (data near5 transfer) and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/12 16:33
	628	(co\$1processor (dual adj1 processor)) and ((address and data) adj2 bus) and (data near5 transfer) and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/13 08:20
	1081	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor))	USPAT; US-PGPUB	2002/08/13 08:49
	598	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor)) and (control near2 register)	USPAT; US-PGPUB	2002/08/13 08:50
	567	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 08:55
	489	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 09:05
	202	(data near5 transfer) same burst and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 10:29
	234	712/225.ccls.	USPAT; US-PGPUB	2002/08/13 09:31
	154	712/225.ccls. and coprocessor co-processor and burst and control adj1 register	USPAT; US-PGPUB	2002/08/13 09:35
	55	712/31.ccls.	USPAT; US-PGPUB	2002/08/13 09:35
	196	(data near2 transfer) same burst and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 10:31
	337	(data near2 transfer) and coprocessor co-processor (dual adj1 processor)	EPO; JPO	2002/08/13 10:33

-	250	(data near2 transfer) and coprocessor co-processor (dual adj1 processor) and burst and (address near2 bus) and (data near2 bus)	EPO; JPO	2002/08/13 10:37
-	619	dual adj processor	USPAT; US-PGPUB	2002/08/13 10:38
-	224	dual adj processor and data adj transfer	USPAT; US-PGPUB	2002/08/13 10:39
-	40	dual adj processor and data adj transfer and (co-processor coprocessor)	USPAT; US-PGPUB	2002/08/13 10:39
-	22	712/34.ccls. and burst	USPAT; US-PGPUB	2002/08/13 15:48
-	22	712/34.ccls. and burst	USPAT; US-PGPUB	2002/08/13 15:13
-	1	6321310.pn.	USPAT; US-PGPUB	2002/08/13 15:48
-	124	(coprocessor co-processor) and (internal adj1 memory) and (control adj1 register) and burst	USPAT; US-PGPUB	2002/08/13 16:15
-	28	712/32.ccls. and burst	USPAT; US-PGPUB	2002/08/15 10:09
-	6	el-kik.in.	USPAT; US-PGPUB	2002/08/14 09:45
-	13	burst adj mode adj bit	USPAT; US-PGPUB	2002/08/14 11:32
-	16	712/35.ccls. and burst	USPAT; US-PGPUB	2002/08/15 11:10
-	28	712/32.ccls. and burst	USPAT; US-PGPUB	2002/08/15 11:10
-	246	burst and transfer same (coprocessor co-processor)	USPAT; US-PGPUB	2002/08/15 15:00
-	509	@ad<=20000106 and (burst with transfer) and ((master and slave) co-processor coprocessor) and (register with control) and (bit with (mode burst))	USPAT; US-PGPUB	2002/12/12 09:03
-	514	@ad<=20000106 and (burst with transfer) and ((master and slave) co-processor coprocessor) and (register with control) and ((bit flag) with (mode burst))	USPAT; US-PGPUB	2002/12/12 09:03
-	67	@ad<=20000106 and (burst with transfer) same ((master and slave) co-processor coprocessor) and (register with control) and ((bit flag) with (mode burst))	USPAT; US-PGPUB	2002/12/12 14:49